Recipi date: 01/30/2006



IN THE UNITED STATES DESIGNATED OFFICE TO JEOUS SO JAN 2000

ATTY DKT. # ONS00396

Date
IN RE APPLICATION OF

APPLN. NO.

FOR CIRCUIT 1/24/2006 François LHermite

PCT/US2003/030581 9/23/2003

: 9/23/2003 : NEGATIVE CURRENT COMPENSATION METHOD AND

DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.56

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SIR:

It is respectfully requested that the prior art listed on FORM PTO-1449 be considered in the examination of the subject application and made of record therein. A copy of the listed prior art is enclosed herewith.

No representation is made or intended that the listed prior art enclosed herewith is material to patentability of the subject patent application.

No representation is made or intended that a search has been made or that no better prior art than that listed is available.

Respectfully submitted, Francois LHermite

ON Semiconductor Intellectual Property Dept. P.O. Box 62890 - A700 Phoenix, AZ 85082-2890

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Sheet 1 of 1

ORM PTC 449	U.S. DEPARTMENT OF COMMERCE		ATTY. DOCKET N ONS00396	О.		SERIAL NO. PCT/US2003/0 30581	
PATENT AND TRADEMARK OFFICE							
	FORMATION DISC		APPLICANT Francois LHermite			-	
(Use several sheets if necessary)			FILING DATE 23 September 200)3	-	GROUP	
_		U.	S. PATENT DOCUM	ENTS			
XAMINE	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
/A.B./	5784231	7/21/1998	Majid et al.	H02H	7/00	-	
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/A.B./		itegrated Circuits " Semiconductors, 23	TEA1533P; TEA153	BAP GreenChip	™II SMPS contr	ol IC", August 23	
/A.B./	Data Sheet, In Semiconductor	tegrated Circuits "	TEA1552 GreenChip	™II SMPS cont	rol IC", August	27 2002, Philips	
EXAMINER /Adolf Berhane/						DATE 08/21 CONSIDERED	